

Recent IBM patents

The following patents were recently issued by the countries in which the inventions were made. For U.S. patents, titles and names supplied to us by the U.S. Patent Office are reproduced exactly as they appear on the original published patent.

France

393,290	J. F. LePennec, J. L. Clara, L. Massiera, P. Jachimczyk, and P. Therias	Memory and Peripheral Chip Select Apparatus
403,716	G. Ungerboeck	Echo Cancelling Device Providing Adjustment of the Echo Canceller Coefficients During Full-Duplex Transmission
424,589	J. P. Nuez, P. Mollier, and P. Tannhof	Self Referenced Current Switch Logic Circuit with a Push-Pull Output Buffer
441,087	A. Pauporte, C. L. Basso, F. R. Kermarec, H. A. Alaiwan, J. Calvignac, and J. Combes	Checkpointing Mechanism for Fault Tolerant Systems
447,731	A. Tracol, D. Orsatti, G. Barucchi, G. Toubol, J. Calvignac, and J. Galcera	A Synchronization Circuit for a Synchronous Switching System
463,269	J. C. Abbiate and L. Quenel	Method and Apparatus for Automatic Functional Speed Setting of a Data Circuit Terminating Equipment

Germany

9,505,130.9	H. Horbach and W. Fischer	Computer for Data Acquisition
9,505,534.7	K. Werse and W. Fischer	XDE-Terminal
68,923,880.0	H. Engelke	Fabrication of Complementary Patterns for Exposing Semiconductor Chips with Self-Supporting Masks
69,112,389.6	G. Koetzle, H. Gruber, H. Horbach, and H. Schettler	Double Surface Cooler Module
69,204,045.5	H. Korth and J. G. Korth	Virtual Optical Keyboard

Italy

481,107	M. Giustiniani and P. Pierucci	A Phonetic Hidden Markov Model Text-to-Speech Synthesizer
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Japan

872,101-2	K. Yamazaki	Notebook PC with Color Display
872,101-3	K. Nakada	Industrial Design of Small Notebook PC (B5 File Size)
893,243-1	H. Shima and K. Yamazaki	Industrial Design of Notebook PC (Pen)
914,877-1	T. Imai, T. Takahashi, and Y. Manabe	21" Color Monitor
1,958,845	H. Sone and K. Sekiya	Emulation of Wired-OR Signals Over Communication Link
1,958,849	M. Noda	Apparatus and Method for Detecting Line Segment Direction
1,964,010	H. I. Iiyori and S. Takayama	High Recording Density Magneto-Optical Media

United Kingdom

396,834	P. R. MacFarlane	Version-Control in a Data-Processing System
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439,873	A. N. Other, A. S. Murphy, J. A. Woolsey, and P. Tustin	Data Processing System
484,603	C. Holloway	Non-repudiation in Computer Networks
487,796	J. S. Beeteson and K. G. Smith	Cathode Ray Tube Display
2,047,609	M. D. Oates	Visual Display Unit
United States		
5,418,738	W. W. Abadeer, B. El-Kareh, W. F. Ellis, D. E. Galbi, N. R. Hildebeitel, W. R. Tonti, and J. S. Watts	Low Voltage Programmable Storage Element
5,418,789	J. E. Gersbach, I. I. Novof, and J. K. Lee	Fast Communication Link Bit Error Rate Estimator
5,420,455	R. A. Gilmour, T. J. Hartswick, D. C. Thomas, R. R. Uttecht, and E. G. Walton	Array Fuse Damage Protection Devices and Fabrication Method
5,420,456	D. E. Galbi, W. H. Guthrie, O. Kiehl, J. A. Mandelman, and J. S. Watts	Zag Ruse for Reduced Blow-Current Application
5,420,467	W. V. Huott and T. G. McNamara	Programmable Delay Clock Chopper/Stretchers with Fast Recovery
5,420,484	A. J. Morrish	Deflection Apparatus for Raster Scanned CRT Displays
5,420,520	M. Ansel, A. P. Ingraham, C. R. Lamb, M. D. Lowell, V. R. Markovich, W. Mayr, R. G. Murphy, M. V. Pierson, T. A. Powers, T. S. Reny, S. D. Reynolds, B. G. Sammakia, and W. R. Storr	Method and Apparatus for Testing of Integrated Circuit Chips
5,420,608	S. M. Choi, L. Lumelsky, A. W. Peevers, and J. L. Pittas	Frame Buffer Organization and Control for Real-Time Image Decompression
5,420,727	R. B. Basham, J. M. Kiser, and A. S. Patapoutian	Error Controlled Volume Serial Identification for Varied Track Density Systems
5,420,736	D. E. Heim and P.-K. Wang	MR Read Transducer with Thermal Noise Cancellation
5,420,760	D. F. Ansell, J. W. Benck, J. C. Harris, S. E. Howell, B. A. Trumbo, and R. D. Wysong	Microcomputer Enclosure with Interrupted Wedge Locking Arrangement and Shielding Liner
5,420,801	M. J. Dockter, C. I. Haug, and K. D. Seppi	System and Method for Synchronization of Multimedia Streams
5,420,834	B. I. Finkelstein, A. A. Gaudet, T. W. McDaniel, H. J. Rosen, K. A. Rubin, and T. C. Strand	Optical Disk Recorders Using Disks Coatings Close to Outer Surfaces with Optical Path Extending Through a Thick Transparent Substrate
5,420,867	S. Price, D. J. White, and B. J. Worley	ISDN Call Processing
5,420,888	G. T. Davis, B. D. Mandalia, and J. C. Sinibaldi	System and Method for Split Phase Demodulation of Frequency Shift Keyed Signals
5,420,893	T. J. Ward	Asynchronous Data Channel for Information Storage Subsystem
5,420,936	G. P. Fitzpatrick, T. R. Haynes, and M. L. Williams	Method and Apparatus for Accessing Touch Screen Desktop Objects via Fingerprint Recognition
5,420,967	H. R. Delp	Method and Apparatus for the Efficient Generation of High Quality Images in a Computer System Utilizing an Interconnected Image State Table
5,420,968	A. Johri	Data Processing System and Method for Displaying Dynamic Images Having Visual Appearances Indicative of Real World Status
5,420,972	L. E. Johnson, Jr. and D. J. Kokoszka	Method and Apparatus for Rendering Lines
5,420,974	J. K. Morris, R. E. Probst, and R. A. Utley	Multimedia Complex Form Creation, Display and Editing Method Apparatus
5,420,975	J. A. Blades and H. G. Kiel	Method and System for Automatic Alteration of Display of Menu Options
5,420,976	D. J. Schell and L. A. Melkus	Method for Selecting Position-Dependent Actions of Computer Applications Programs
5,420,978	Y. Tozawa and S. Yokoi	Method and Apparatus for Customizing a Schedule Display
5,420,988	J. C. Elliott	Establishing Logical Paths Through a Switch Between Channels and Control Units in a Computer I/O System
5,420,999	P. Mundy	Free Memory Cell Management
5,421,000	R. N. Fortino, H. I. Linzer, and K. E. O'Donnell	Memory Subsystem Having a Static Row Memory and a Dynamic RAM
5,421,003	D. R. Escola, S. D. Gerdt, B. N. Harding, and L. R. Shipman, Jr.	Disk Storage System with Fault Tolerant Media Maintenance
5,421,004	E. R. Carpenter, C. S. Claussen, and J. O. Cox	Hierarchical Testing Environment
5,421,008	K. R. Banning, W. S. James, and S.-G. Li	System for Interactive Graphical Construction of a Data Base Query and Storing of the Query Object Links as an Object
5,421,011	N. A. Camillone, D. H. Steves, and K. C. Witte	Method and System for Access and Accounting Control in a Data Processing System by Using a Single Resource Account for a User or a Group of Users

5,421,016	M. H. Conner, N. Coskun, A. R. Martin, and L. K. Raper	System and Method for Dynamically Invoking Object Methods from an Application Designed for Static Method Invocation
5,421,020	D. S. Levitan	Counter Register Implementation for Speculative Execution of Branch on Count Instructions
5,421,079	T. M. Cipolla, P. W. Coteus, and A. P. Lanzetta	High Density, High Performance Memory Circuit Package
5,421,507	C. R. Davis, R. Hsiao, J. R. Loomis, J. M. Park, and J. D. Reid	Au-Sn Transient Liquid Bonding in High Performance Laminates
5,421,723	R. P. Katz, W. W. Olah, and W. B. Roberts	Sequential Step Belt Furnace with Individual Concentric Cooling Elements
5,421,943	A. C. Tam and C. E. Yeack-Scranton	Pulsed Current Resistive Heating for Bonding Temperature Critical Components
5,422,184	K. I. Papathomas	Trifunctional Cyanate Esters, Polymers Thereof; Use and Preparation Thereof
5,422,223	K. G. Sachdev, H. S. Sachdev, and J. R. Whitaker	Silicon-Containing Positive Resist and Use in Multilayer Metal Structures
5,422,571	B. A. Gurney, D. E. Heim, H. Lefakis, O. U. Need III, V. S. Speriosu, and D. R. Wilhoit	Magnetoresistive Spin Valve Sensor Having a Nonmagnetic Back Layer
5,422,603	M. Soyuer	CMOS Frequency Synthesizer
5,422,621	R. J. Gambino, J. M. E. Harper, T. R. McGuire, and T. S. Plaskett	Oriented Granular Giant Magnetoresistance Sensor
5,422,642	P. W. Chung, J. E. Gersbach, B. Pham, K. Hense, and P. Granata	DC Centering Analog Receiver for Flash A/D Converter
5,422,656	D. J. Allard, B. A. Beatty, F. J. Canova, Jr., B. J. DeBauche, D. M. Hsieh, D. A. G. Johnson, C. S. Lanier, J. R. Lewis, B. K. Tiller, W. Villafana, and R. L. Yee	Personal Communicator Having Improved Contrast for a Liquid Crystal, Touch Sensitive Display
5,422,761	N. C. Anderson, M. A. Hajji, H. H. Ottesen, and M. J. Ross	Disk Drive with Redundant Recording
5,422,767	M. R. Hatchett, J. S. Heath, H. C. Lee, E. K. Row, R. D. Spencer, and M. L. Workman	Vibration Damper for a Multiple Disk Drive Unit
5,422,835	R. M. Houle and D. C. Pham	Digital Clock Signal Multiplier Circuit
5,422,883	J. F. Hauris, D. M. Kalajainen, and R. A. Bowen	Call Setup and Channel Allocation for a Multi-Media Network Bus
5,422,893	T. A. Gregg, J. M. Hoke, A. Ing, and C. Lee	Maintaining Information from a Damaged Frame by the Receiver in a Communication Link
5,422,898	J. A. Kash, D. W. Kisker, B. Pezeshki, and F. F. Tong	Tapered Fabry-Perot Multi-Wavelength Optical Source
5,422,988	A. Koide	Method and Apparatus for Rendering a Three-Dimensional Object with a Plurality of Dots
5,422,991	M. C. Fowler	Parallel Vector Generator and Triangle Generator Incorporating Same
5,422,993	S. S. Fleming	Method and System for Performing Direct Manipulation Operations in a Computer System
5,422,995	Y. Aoki, Y. Gohda, and D. P. Rackley	Method and Means for Fast Writing of Run Length Coded Bit Strings into Bit Mapped Memory and the Like
5,423,006	P. J. Brown, J. C. Elliott, K. H. Hoppe, K. R. Lynch, and M. W. Sachs	Notification and Verification of State Changes in a Data Processing Input/Output System
5,423,011	B. Blaner and S. Vassiliadis	Apparatus for Initializing Branch Prediction Information
5,423,013	R. I. Baum, B. A. Carlson, M. J. Kim, M. G. Mall, C. A. Scalzi, and B. Sinha	System for Addressing a Very Large Memory with Real or Virtual Addresses Using Address Mode Registers
5,423,017	S. N. Parikh	Method of and Apparatus for Increasing Efficiency of Ager
5,423,018	C. H. Dang, M. T. Solan, and D. J. Winarski	Queue Time Reduction in a Data Storage Hierarchy Using Volume Mount Rate
5,423,020	T. B. Vojnovich	Apparatus and Method for Optimizing Bus Usage by Varying the Amount of Data Transferred on a DMA Operation
5,423,026	T. E. Cook, M. J. Halma, A. S. Meritt, and H. M. Yudenfriend	Method and Apparatus for Performing Control Unit Level Recovery Operations
5,423,032	R. J. Byrd, J. S. Justeson, and S. M. Katz	Method for Extracting Multi-Word Technical Terms from Text
5,423,040	D. A. Epstein, G. G. Gilley, and K. P. McAuliff	System and Method for Efficiently Executing Directed Acyclic Graphs
5,423,042	R. Jalili and W. L. Wooten	Remote Procedure Execution
5,423,043	G. P. Fitzpatrick and M. L. Williams	Method and Apparatus for Creating and Monitoring Logical Associations Among Desktop Objects
5,423,044	P. G. Sutton, I. J. Kupfer, G. W. Sears, Jr., T. Goetz, and R. O. Dryfoos	Shared, Distributed Lock Manager for Loosely Coupled Processing Systems

5,423,045	K. Kannan, C. D. Jones, N. J. Lee, K. Leontiades, F. P. Novak, and V. Sharma	System for Distributed Power Management in Portable Computers
5,423,046	L. L. Nunnolley, L. L. Williams, and L. C. Wood, Jr.	High Capacity Data Storage System Using Disk Array
5,423,051	R. A. Fuller, A. K. Ghose, F. W. Sell, and M. S. Siegel	Execution Unit with an Integrated Vector Operation Capability
5,423,207	K. A. Flechsig, C.-K. Lee, S. L. Lee, M. L. McGhee, U. V. Nayak, T. C. O'Sullivan, and J. Walian	Advanced PZT Glide Head Design and Implementation for a Small Slider
5,423,690	E. J. Selker and W. M. Dyer	Universal Electrical Power Plug for Multination Use with Self-Setting Contact Pins
5,423,940	L. Chen, S.-J. Jeng, W. C. Natzle, and C. Yu	Supersonic Molecular Beam Etching of Surfaces
5,424,054	D. S. Bethune, R. B. Beyers, and C.-H. Kiang	Carbon Fibers and Method for Their Production
5,424,254	P. Damiot	Process for Recovering Bare Semiconductor Chips from Plastic Packaged Modules by Thermal Shock
5,424,261	J. H. Harris, S. L. Shinde, T. Takamori, and R. A. Youngman	Low Temperature Sintering Route for Aluminum Nitride Ceramics
5,424,548	D. M. Puisto	Pattern Specific Calibration for E-Beam Lithography
5,424,634	S. M. Goldfarb, P. R. Herb, J. M. Lukaitis, and L. Shi	Non-Destructive Flex Testing Method and Means
5,424,659	G. B. Stephens and S. J. Tucker	Mixed Voltage Output Buffer Circuit
5,424,662	A. R. Bonaccio	Differential Current Mode Driver Circuit with Low Common-Mode Noise
5,424,724	M. L. Williams and R. H.-C. Yun	Method and Apparatus for Enhanced Electronic Mail Distribution
5,424,732	B. R. Iyer, T. A. Meriwether, E. B. Sherwin, Jr., and B. Sinha	Transmission Compatibility Using Custom Compression Method and Hardware
5,424,888	F. Hendriks, V. D. Khanna, and R. M. Crone	Speed Independent, Air Bearing Slider
5,424,947	K. Nagao and H. Nomiyama	Natural Language Analyzing Apparatus and Method, and Construction of a Knowledge Base for Natural Language Analysis
5,425,013	A. A. Fennema, T. S. Gardner, B. G. Goodman, and Y. Hirasaka	Relative Position Sensing Calibration for Optical Storage Device
5,425,020	T. A. Gregg, J. M. Hoke, and P. J. Sugrue	Skew Measurement for Receiving Frame-Groups
5,425,021	J. H. Derby, J. E. Drake, Jr., J. G. Dudley, R. Guerin, M. A. Kaplan, G. A. Marin, M. L. Peters, and K. H. Potter, Jr.	Packet Switching Resource Management within Nodes
5,425,028	K. H. Britton, T.-Y. D. Chung, W. A. Doeringer, D. H. Dykeman, A. K. Edwards, J. Mathew, D. P. Pozefsky, S. Sarkar, and R. D. Turner	Protocol Selection and Address Resolution for Programs Running in Heterogeneous Networks
5,425,038	C.-L. Chen	Error Plus Single Bit Error Detection
5,425,129	J. H. Garman, A. G. Klein, and V. M. Stanford	Method for Word Spotting in Continuous Speech
5,425,140	M. A. Bloomfield and S. A. Klinger	Method and Apparatus for Providing Conditional Cascading in a Computer System Graphical User Interface
5,425,493	M. J. Interrante and L. Economikos	Selective Addition of a Solder Ball to an Array of Solder Balls
5,425,810	R. A. Conti, D. E. Kotecki, D. L. Wilson, J. W. Wong, and S. P. Zuhoski	Removable Gas Injectors for Use in Chemical Vapor Deposition of Aluminum Oxide
5,426,311	R. A. Webb	Solid-State Variable-Conductance Device
5,426,316	S. N. Mohammad	Triple Heterojunction Bipolar Transistor
5,426,324	T. V. Rajeevakumar	High Capacitance Multi-Level Storage Node for High Density TFT Load SRAMs with Low Soft Error Rates
5,426,330	R. V. Joshi, J. J. Cuomo, H. M. Dalal, and L. L. Hsu	Refractory Metal Capped Low Resistivity Metal Conductor Lines and Vias
5,426,430	E. S. Schlig	Pipelined Charge Metering Digital-to-Analog Data Line Driver
5,426,541	J. D. Coker, R. L. Galbraith, W. Hirt, D. J. Stanek, and M. D. Warne	Self-Equalization Method for Partial-Response Maximum-Likelihood Disk Drive Systems
5,426,566	K. E. Beilstein, Jr., C. L. Bertin, H. L. Kalter, G. A. Kelley, Jr., C. P. Miller, D. E. Pontius, W. B. van der Hoeven, and S. Platt	Multichip Integrated Circuit Packages and Systems
5,426,581	G. T. Kishi and S. H. Obeid	Using a Bar Code Scanner to Calibrate Positioning of a Robotic System
5,426,625	N. X. Bui, A. A. Fennema, and R. A. Klem	Masking of Tracking Error Signal Abnormalities Due to Media Defects

5,426,637	J. H. Derby, W. A. Doeringer, H. D. Dykeman, L. Li, H. J. Sandick, and K. V. Vu	Methods and Apparatus for Interconnecting Local Area Networks with Wide Area Backbone Networks
5,426,655	E. Feig	Method and Apparatus for Magnetic Recording of Data
5,426,711	K. Kitamura	Online Handwritten Character Recognition
5,426,732	S. J. Boies, L. D. Comerford, J. D. Gould, S. L. Spraragen, and J. P. Ukelson	Method and Apparatus for User Control by Deriving Next States of a Process from a Current State and by Providing a Visual Presentation of the Derived Next States
5,426,743	J. E. Phillips and S. Vassiliadis	3-1 Arithmetic Logic Unit for Simultaneous Execution of an Independent or Dependent Add/Logic Instruction Pair
5,426,748	J. G. Brenza, J. M. Gdaniec, P. H. Gum, K. M. Jackson, M. M. Maccabee, C. A. Scalzi, and B. Sinha	Guest/Host Extended Addressing Method and Means with Contiguous Access List Entries
5,426,754	D. G. Grice, D. W. Price, and R. S. Raji	Cross-Interrogate Method and Means for Combined Scaler and Vector Processing System
5,426,758	S. K. Candelaria, D. L. Hanson, R. L. Kwok, K. W. Lane, D. M. Nordahl, M. A. Reid, and W. G. Sherman II	Method and System for Accessing Data from a Multi-Head Data Processing System Utilizing a Transducer Head Switching Table
5,426,761	J. H. Cord, S. K. Candelaria, J. S. Hyde, and L. R. Perry	Cache DASD Sequential Staging and Method
5,426,773	A. Chabanet, J.-J. Chartreux, E. Lebrun, G. Platel, and A. Rey	Communication Controller Allowing Continued Communication Through an X25 Network and an SNA Network in the Event of an SNA Session Failure
5,426,781	C. A. Kaplan, S. E. Taylor, and G. J. Wolff	Computerized Report-Based Interactive Database Query Interface
5,427,599	J. Greschner, G. Schmid, W. Steiner, G. Trippel, and O. Wolter	System for Stamping an Optical Storage Disk
5,427,622	D. Stanasolovich, W. A. Syverson, and R. A. Warren	Method for Uniform Cleaning of Wafers Using Megasonic Energy
5,427,627	M. J. Canestaro, D. G. McBride, L. J. Konrad II, and R. J. Moore	Method for Treating Substrates
5,427,630	C. Cabral, Jr., K. K. Chan, J. O. Chu, and J. M. E. Harper	Mask Material for Low Temperature Selective Growth of Silicon or Silicon Alloys
5,427,848	L. J. Baer, P. G. Rickerl, and M. J. Schadt	Stress Balanced Composite Laminate Material
5,427,895	R. H. Magnuson, R. W. Malek, V. R. Markovich, and W. E. Wilson	Semi-Subtractive Circuitization
5,427,983	U. M. U. Ahmad, H. S. Bhatia, S. P. S. Bhatia, H. M. Dalal, W. H. Price, and S. Purushothaman	Process for Corrosion Free Multi-Layer Metal Conductors
5,428,506	M. J. Brown, L. C. Radzik, J. D. Williams, and O. D. Pitts	Circuit Board EMI Suppressor Including a Lossy Dielectric Layer
5,428,529	T. V. Hartrick, P. E. Higgins, and N. J. Sabia	Structured Document Tags Invoking Specialized Functions
5,428,552	J. R. Rudert, Jr. and R. A. Simpson	Data Compaction Techniques for Generation of a Complex Image
5,428,554	G. M. Laskoski	Hierarchical Graph Analysis Method and Apparatus
5,428,567	T. A. Horvath, M.-H. Lin, and G.-G. Mei	Memory Structure to Minimize Rounding/Truncation Errors for n-Dimensional Image Transformation
5,428,628	M. Hassner, U. Schwiegelshohn, and S. Winograd	Modular Implementation for a Parallelized Key Equation Solver for Linear Algebraic Codes
5,428,645	D. Dolev, R. K. Reischuk, and H. R. Strong	Anonymous Time Synchronization Method
5,428,649	D. R. Cecchi	Elastic Buffer with Bidirectional Phase Detector
5,428,678	G. P. Fitzpatrick, W. E. Warren, and M. L. Williams	Telephone Calling Method Apparatus
5,428,694	T. S. Betts and V. M. Carras	Data Processing System and Method for Forms Definition, Recognition and Verification of Scanned Images of Document Forms
5,428,716	D. A. Brokenshire, A. E. Megahed, J. R. Rossignac, and B.-O. Schneider	Solid-Clip Methodology and Architecture for Clipping Solid Models and Displaying Cross-Sections Using Depth-Buffers
5,428,723	F. Ainscow and K. J. Mashford	Method and Apparatus for Capturing the Motion of an Object in Motion Video
5,428,729	R. A. Chang, A. J. Smith, and R. J. Torres	System and Method for Computer Aided Software Engineering
5,428,730	R. S. Baker, R. E. DuLaney, P. A. Esack, S. R. Henson, S. R. Swix, G. E. Wagener, F. D. Warinner, and S. J. Broussard	Multimedia System Having Software Mechanism Providing Standardized Interfaces and Controls for the Operation of Multimedia Devices
5,428,732	S. M. Hancock, M. J. Paulat, Jr., and J. E. Parsons, Jr.	Playlist Mechanism for Specification of Complex Memory Objects
5,428,734	T. R. Haynes, D. J. Kahl, L. A. Melkus, and D. J. Schell	Method and Apparatus for Enhancing Drag and Drop Manipulation of Objects in a Graphical User Interface

5,428,735	D. J. Kahl, C. D. King, R. E. Lee, S. Stanners, and R. J. Torres	Method and Apparatus for Maintaining a Record of Set-Creating Data Processing Activities and Associated Data Sets
5,428,736	D. J. Kahl and R. J. Torres	Method and Apparatus for Entering and Editing Data of a Data Icon in an Enlarged and Minimized Size
5,428,737	S.-G. Li, C. K. Bucher, and K. R. Banning	Comprehensive Bilateral Translation Between SQL and Graphically Depicted Queries
5,428,757	P. G. Sutton	Method for Reducing Translation Look Aside Buffer Purges in a Multitasking System
5,428,762	B. W. Curran and J. L. Temple III	Expandable Memory Having Plural Memory Cards for Distributively Storing System Data
5,428,771	D. S. Daniels, T. J. Freund, R. L. Haskin, and R. A. Storey	Transparent Transaction Coordination Between Distributed Networks Having Different Communication Protocols
5,428,774	J. Takahashi and H. Hashihara	System of Updating an Index File of Frame Sequences So That It Indexes Non-Overlapping Motion Image Frame Sequences
5,428,780	L. E. England, A. J. Lang, and A. W. Shannon	Initializing Programs Having Dependencies
5,428,781	M. Duault and P. Pignal	Distributed Mechanism for the Fast Scheduling of Shared Objects and Apparatus
5,428,784	R. B. Cahill, Jr.	Method and Apparatus for Linking Electronic Mail and an Electronic Calendar to Provide a Dynamic Response to an Electronic Mail Message
5,428,792	M. H. Conner, A. R. Martin, and L. K. Raper	System for Producing Language Neutral Objects and Generating an Interface Between the Objects and Multiple Computer Languages
5,428,795	W. J. Johnson and L. M. Lachman	Method of and Apparatus for Providing Automatic Security Control of Distributions With a Data Processing System
5,428,802	J. S. Anglin and L. R. Shipman, Jr.	Method and Apparatus for Executing Critical Disk Access Commands
5,431,734	J. D. Chapple-Sokol, R. A. Conti, J. A. O'Neill, N. V. Sarma, D. L. Wilson, and J. W.-C. Wong	Aluminum Oxide Low Pressure Chemical Vapor Deposition (LPCVD) System-Fourier Transform Infrared (FTIR) Source Chemical Control
5,431,772	W. T. Babie, K. L. Devries, B. C. Nguyen, and C.-H. J. Yang	Selective Silicon Nitride Plasma Etching Process
5,431,777	L. W. Austin, H. G. Linde, and J. S. Nakos	Methods and Compositions for the Selective Etching of Silicon
5,432,047	J. C. Cheng, W. D. Hinsberg III, R. T. Lynch, Jr., S. A. MacDonald, L. A. Pederson, and J. S. Wong	Patterning Process for Bipolar Optical Storage Medium
5,432,386	P. A. Cerra, Jr. and C. V. Zenz, Sr.	Battery Back-Up Control Circuit for Battery Backed Up Rack Mounted Devices
5,432,411	J. Beeteson and A. Knox	Electric Field Emission Reduction Apparatus
5,432,460	E. Flecha, K. D. Henry, J. C. Mahlbacher, M. Servedio, and K. S. Wienaug	Apparatus and Method for Opens and Shorts Testing of a Circuit Board
5,432,531	G. J. Calder and G. D. Beardall	Coordinate Processor for a Computer System Having a Pointing Device
5,432,652	W. T. Comeaux, D. C. Graves, and D. W. Johnson	Servo and Data Format for Magnetic Tape Having Plural Spaced-Apart Servo Areas Interleaved with Data Track Areas Having Serpentine Track Scanning Using Any One of a Plurality of Number of Concurrently Accessed Tracks
5,432,670	J. S. Batchelder, V. P. Gross, R. A. Gruver, P. C. D. Hobbs, and K. D. Murray	Generation of Ionized Air for Semiconductor Chips
5,432,707	Y. M. Leung	Automated Circuit Design
5,432,714	V. M. Chung, J. B. Frei, and J. E. Stuart	System and Method for Preparing Shape Data for Proximity Correction
5,432,720	S. A. M. Lucente, P. S. Balasubramanian, R. F. Sapper, and N. J. Lee	Rotatable Pen-Based Computer
5,432,777	J.-Y. LeBoudec and L. Truong	Connectionless ATM Data Services
5,432,816	F. Gozzo	System and Method of Robust Sequence Estimation in the Presence of Channel Mismatch Conditions
5,432,848	A. S. Butter, C. Y. Kao, and J. P. Kuruts	DES Encryption and Decryption Unit with Error Checking
5,432,892	J. L. Hafner and W. E. Plouffe, Jr.	Volumetric Linear Interpolation
5,432,898	L. A. Curb, C. Narayanaswami, and A. Saha	System and Method for Producing Anti-Aliased Lines
5,432,908	M. Heddes and R. Luijten	High Speed Buffer Management of Share Memory Using Linked Lists and Plural Buffer Managers for Processing Multiple Requests Concurrently